

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
(Established by the State Legislature Act XII of 1956)  
("A+" Grade NAAC Accredited)

Date-sheet for the M.Sc. Electronics Science II Semester and M. Tech Microelectronics & VLSI  
Design II Semester Examinations May/June, 2023.

(THEORY EXAMINATION)

Examination Centre : As mentioned in Admit Card

Time of Examination : 9.30 A.M. ( MORNING SESSION)

DATE	SUBJECT AND PAPER
	<b>M.Sc. II Semester</b>
2-Jun-2023	EL-21, Digital Circuits and System Design
5-Jun-2023	EL-22, Device Models and Circuits Simulation
8-Jun-2023	EL-23, Verilog-Hardware Description Language
12-Jun-2023	EL-24, System Design Using Embedded Processors
16-Jun-2023	EL-25 Opt(i), Foundations of MEMS
16-Jun-2023	EL-25 Opt(ii), Nano Electronics-Materials & Devices
	<b>M.Tech. Microelectronics &amp; VLSI Design II Semester</b>
2-Jun-2023	MMVD-201: Process Technology for VLSI-II
5-Jun-2023	MMVD-202: Embedded System Design Using 8051
8-Jun-2023	MMVD-203: Analog CMOS Integrated Circuits
12-Jun-2023	MMVD-204: Verilog Hardware Description Language

- NOTES :
- 1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.
  - 2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.
  - 3 No extra answer-sheet will be provided.

KURUKSHETRA  
17-Apr-2023

CONTROLLER OF EXAMINATIONS